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10/603,055	06/24/2003	2835	1130	32079-00087USPX	20	28	4

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**CONFIRMATION NO. 8552**  
**CORRECTED FILING RECEIPT**  
**\*OC000000017061115\***  
 \*OC000000017061115\*

Date Mailed: 09/20/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

**Applicant(s)**

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**Domestic Priority data as claimed by applicant**

**Foreign Applications**

EUROPEAN PATENT OFFICE (EPO) 02425415.3 06/25/2002

**If Required, Foreign Filing License Granted: 09/11/2003**

**The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US10/603,055**

**Projected Publication Date: Not Applicable**

**Non-Publication Request: No**

**Early Publication Request:** No

**Title**

Test bench generator for integrated circuits, particularly memories

**Preliminary Class**

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